

Fig. 1

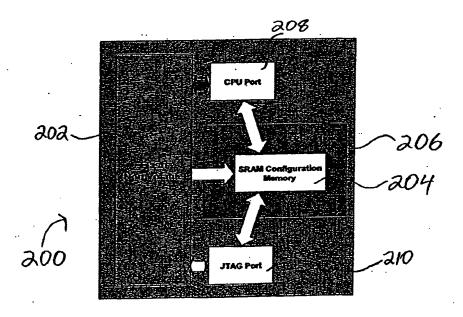


Fig. 2

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Modes used for programming and configuring the memory spaces

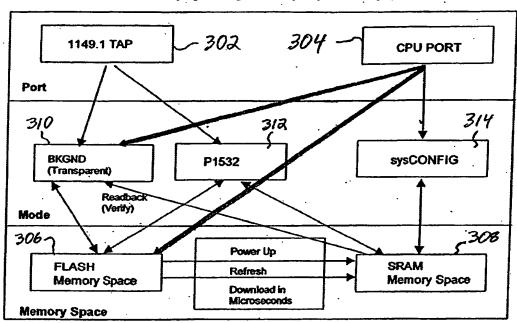
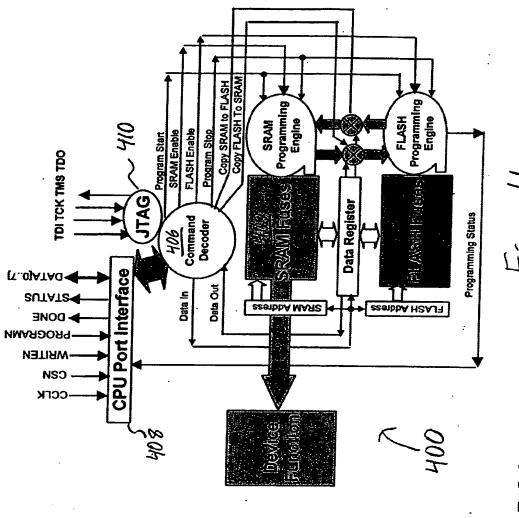


Fig. 3



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